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Susan P. Van Holstyn

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Johannes Stollenwerk et al.

For : CONDUCTIVE TRANSPARENT LAYERS AND A METHOD
FOR THEIR PRODUCTION

Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

Dear Sir:

Enclosed herewith is the above-identified patent application comprising the following parts:

- 1) Postcard
- 2) 6 Pages of Specification
- 3) 4 Pages of Claims (25 claims)
- 4) 1 Page of Abstract
- 5) 1 Sheet of Drawings (in duplicate)

The Filing Fee, as calculated below, is not being enclosed since the executed Small Entity Declaration is not yet available.

- 6) Filing Fee:

Basic Fee	\$355.00	\$355.00
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Additional Fees

Each independent claim in excess of three, times \$40.00	\$000.00
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Number of claims in excess of twenty, times \$9.00	\$ 45.00
Filing multiple dependent claims per application \$135.00	\$000.00
Total Filing Fee	\$400.00

Pursuant to 37 C.F.R. 1.53, our payment in the amount of \$400.00 to cover the filing fees noted above will be submitted in response to the PTO-1553 Notice to File Missing Parts of Application.

The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application, or credit any overpayment, to Deposit Account No. 16-2463. A duplicate of the Authorization is also enclosed.

- 1) Any filing fees under 37 C.F.R. 1.16 for presentation of extra claims for which full payment has not been tendered.
- 2) Any patent application processing fees under 37 C.F.R. 1.17 for which full payment has not been tendered.
- 3) Any assignment recording fee under 37 C.F.R. 1.21 for which payment has not been tendered.

Respectfully submitted,
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CONDUCTIVE TRANSPARENT LAYERS AND METHOD FOR THEIR PRODUCTION

FIELD OF THE INVENTION

This invention relates to conductive transparent layers and to a method for the
5 production of these conductive transparent layers.

BACKGROUND OF THE INVENTION

Conductive transparent layers today have many applications in display devices, optoelectronics and as architectural glass. For these applications it is desirable, on the one hand, that transmittance in the visible part of the spectrum is as high as possible, and on the
10 other hand, that conductivity is as high as possible or surface resistivity as low as possible. As a measure of the quality of conductive transparent layers, the Haacke quality factor $\Phi_{TC} = T^{10}/R_s$ can be used, which is defined in the *Journal of Applied Physics*, Vol. 47, pages 4086-4089 (1976). "T" stands for the optical transmittance of the layer (as a fraction of the incident radiation) and R_s for the surface resistivity in Ω . For example, a layer with a transmittance of
15 90% and a surface resistivity of 3Ω has a Haacke quality factor of 0.116^{-1} . A layer with a transmittance of 80% and a surface resistivity of 5Ω has a quality factor of 0.021^{-1} .

Another important property of such a layer system is its etching performance. This depends on its chemical composition and its thickness. For a short etching time and good edge sharpness, it is important that the layer is as thin as possible, i.e. less than 100 nm.

20 To obtain high quality factors, it is beneficial to combine layer systems of oxidic and metallic layers. For example, the interposition of very thin layers of silver between thin oxide layers is known. As a result of being sandwiched between oxide layers, the silver layer is stabilized and protected on the one hand, while on the other hand, its reflection is reduced and transmittance thus increased. These layer combinations have the additional advantage of a low
25 overall layer thickness, namely 100 nm or less. A layer system of indium tin oxide, by comparison, which is of comparable resistivity, is more than 500 nm thick (S. H. Shin and co-authors, *Thin Solid Films* 341 (1999) 225-229). Consequently, etching processes of the kind typically used for manufacturing displays are faster and the extent of undercut is less.

Layer systems of this kind are described, for example, in: DP 0 599 071 A1, JP 10062602 A and in the article by K. K. Choi and co-authors, Thin Solid Films 341 (1999) 152-155.

In the EP 0 599 071 A1 a layer system is described with the layer sequence indium tin oxide, silver or various silver alloys, indium tin oxide. By annealing at 300° C. for an hour, layers can be produced with a surface resistivity of 3.2 Ω and, at the same time, good transmittance in the visual part of the spectrum. For the wavelengths 435, 545 and 610 nm, a mean Haacke quality factor of 0.066 is obtained. The subsequent temperature treatment necessary for display applications is, however, a disadvantage, since it entails an additional 10 step.

In the JP 10062602 A a similar layer system is described. Here, a thin silver layer containing at least 1.5 atomic percentage added gold is embedded between oxide layers consisting of tin oxide, indium oxide and small additions of other oxides. Layers are obtained which have a surface resistivity of 4-20 Ω and high transparency at 550 nm. The higher costs 15 incurred due to addition of gold and the relatively high surface resistivity must be seen as disadvantages.

In Thin Solid Films 341, K. K. Choi and co-authors describe a layer system consisting of indium tin oxide followed by a silver layer and, as covering layer, indium tin oxide again. To improve conductivity, the indium tin oxide layers are deposited at 200° C., but the silver 20 layer at room temperature. However, as a result of heating prior to deposition of the second layer of indium tin oxide, the optical transmittance and the electrical conductivity of the silver layer are impaired. In the best of cases, layers with a surface resistivity of 4 Ω and 90% transmittance at 550 nm are obtained.

It is also known that through selective choice of materials and coating parameters, 25 transparent, conductive layer systems can be produced which have a resistivity of 2.93 Ω , transmittance values (measured against air) of 89.2% at 435 nm, 92.4% at 545 nm and 82.2% at 610 nm, and an overall layer thickness of 86.5 nm. For the three wavelengths quoted, this transparent conductor has a mean Haacke quality factor of 0.104 Ω^{-1} .

In the field of display devices, transparent electrodes with even lower surface resistivity 30 combined with high transmittance in the visible part of the spectrum, i.e. a high Haacke quality

factor, are needed for large-area flat LCD displays and for computer monitors with screen diagonals preferably over 17". This is necessary because of the image size, the high resolution and pixel count, as well as the speed of these displays. These requirements can no longer be met by the methods known to date.

5

SUMMARY OF THE INVENTION

The object of this invention is to overcome the disadvantages of the prior art and, in particular, to achieve an even lower surface resistivity combined with a high Haacke quality factor.

10 This object is established by means of a layer system according to claim 1, and a method according to claim 6. Additional preferred embodiments of the invention are described in the dependent claims.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a diagram showing a cross section through a layer system according to the invention.

15 Fig. 2 shows the optical transmittance -- measured against air -- of a layer system according to the invention and having a surface resistivity of 2.5 sq in the visible part of the spectrum.

DESCRIPTION OF THE PREFERRED EMBODIMENT

20 A layer system according to the preferred embodiment of the invention comprises at least two oxide layers with a silver layer interposed between them, and has a surface resistivity of less than 2.9 sq , preferably 2.5 sq or less, combined with a Haacke quality factor -- averaged over the wavelengths 435, 545 and 610 nm -- in excess of 0.085^{-1} .

25 For reasons of color neutrality it is beneficial if, for a surface resistivity of 2.5 sq , the optical transmittance at 435 nm is at least 89%, at 545 nm at least 88% and at 610 nm at least 75%. This ensures that the coating appears as neutral as possible.

Especially good results are obtained if the two oxide layers are advantageously selected to be less than 50 nm thick, preferably between 30 and 40 nm, and the silver layer less than 20 nm thick, preferably 15 nm.

30 Glare suppression by the oxide layers is especially good if, in addition to indium, the oxide layer contains up to 10 at. % cerium.

The stability of the silver layer is enhanced by addition of up to 10 wt. % copper.

Additions of 0.5 to 3%, and, in particular, 0.5 to 1%, are especially effective.

During production of the layer systems described it is of crucial importance, as described in claim 6 and additional dependent claims, that the second oxide layer is not deposited by pure DC sputtering but by pulsed DC sputtering or AC-superimposed DC sputtering. AC-superimposition can be effected, for example, by coupling the output signal via a filter to the sputter source supplied with DC current. An alternative method is, for example, to suitably modulate or chop the DC supply current. Different modulations are thus possible.

To achieve especially good results, the AC frequency should be between 1 and 50 MHz, preferably between 10 and 20 MHz.

It is also beneficial if the AC component, defined by the ratio of the DC and AC power supplies, is adjusted between 10 and 90%, preferably between 30 and 50%.

A total power density (AC and DC) of 1 to 3 W/cm², preferably of 2 to 2.2 W/cm², proved especially suitable.

The preferred sputtering method is magnetron sputtering.

The advantages of the method of the invention can be summarized as follows:

By maintaining the high conductivity of the thin silver layer by means of the way in which the second oxide layer is applied, optical transmittance can be kept high. Without the method of the invention, it would be necessary, it would be necessary to increase the thickness of the silver layer in order to obtain this conductivity, and doing this would unavoidably lead to a pronounced reduction in transmittance and thus to a significant deterioration in the Haacke quality factor.

The production of such layers using the method of the invention will now be described using the following example.

The glass substrates, of conventional thin float glass or broad window glass, are cleaned in the usual manner and then introduced into a sputtering unit. The vacuum chamber is pumped down and, once the necessary vacuum has been generated, deposition of the first oxide layer comprising indium oxide and cerium oxide begun. This oxide layer is ejected from an oxide target in a partially reactive DC sputtering process, i.e. in an argon atmosphere of approximately 2.2×10^{-3} hPa with a maximum oxygen addition of 5%. This sputtering process

is a purely DC process. Typical sputtering rates are 5-8 nm x m/min x cm²/W. Subsequently, the silver layer is sputter-deposited in a pure, non-reactive DC process. Typical sputtering rates here are 12-15 nm x m/min x cm²/W. In the next step, the second oxide layer is sputter-deposited in an AC-superimposed DC sputtering process. The AC component, defined by the 5 ratio of the DC and AC power supplies, is between 30 and 50%. The AC frequency is 13.56 MHz. After completion of the sputtering process the coated glass substrates are exposed to the air, either by removing them from the chamber through a vacuum lock or by flooding the chamber with air. In a subsequent etching step the substrates are then structured and processed further to displays.

10 In the following, embodiments of the invention are illustrated by reference to Figs. 1 and 2.

In Fig. 1, reference number 1 indicates the glass substrate onto which the layer system of the invention is deposited, 2 is an indium cerium oxide layer, 3 a copper-doped silver layer and 4 a covering indium cerium oxide layer.

15 The glass substrate 1 is, for example, a commercially available float glass of 1.1 mm thickness. However, different glass thicknesses and different types of glass, e.g., broad window glass, can also be used.

20 An oxide layer 2 with a geometrical thickness of 30-37 nm is deposited onto the glass by means of partially reactive DC sputtering from an oxide target consisting preferably of 90- 95 at. % indium and 5-10 at. % cerium.

In a purely DC sputtering process conducted in an argon atmosphere, a silver layer 3 with a thickness of 15 nm and containing 0.5-10% added copper, preferably 0.5-3% and best of all 0.5-1% copper, is deposited on the oxide layer 2.

25 A second indium cerium oxide layer 4, likewise with a geometrical thickness of 30-37 nm, is deposited directly onto the silver/copper layer 3. This is done, however, by means of an AC-superimposed DC sputtering process. The AC component, defined by the ratio of the DC and AC power supplies, is between 10-90%, preferably between 30-50%. The AC frequency is between 1 and 50 MHz, preferably between 10 and 20 MHz.

Deposition of the silver/copper layer may optionally be followed by deposition of a protective layer of oxides of titanium alloys or nickel alloys. This is done by means of DC magnetron sputtering.

In Fig. 2 the optical transparency (measured against air) of a layer system according to
5 the invention and having a surface resistivity of $2,5 \text{ } \Omega_{\text{sq}}$ is shown as a function of the wavelength in the spectral region from 400-800 nm. The transparency at 435 nm is 89.8%, at 545 nm 88.4% and at 610 nm 75.4%. The mean Haacke quality factor for these three wavelengths is $0.092 \text{ } \text{nm}^{-1}$.

The invention claimed is:

1. A conductive transparent layer system with two oxide layers and a silver layer interposed therebetween on a substrate, characterized in that with a surface resistivity R_s of less than $2.9 \text{ } \Omega/\text{sq}$, preferably less than $2.5 \text{ } \Omega/\text{sq}$, the mean Haacke quality factor ($\Phi_{TC} = T^{10}/R_s$) of the layer system for the wavelengths 435, 545 and 610 nm is greater than $0.085 \text{ } \Omega^{-1}$.
2. The layer system of claim 1, characterized in that with a surface resistivity of $2.5 \text{ } \Omega/\text{sq}$, the transparency T at 435 nm is at least 89%, at 545 nm at least 88% and at 610 nm at least 75%.
3. The layer system according to claim 3, characterized in that the layer system is less than 100 nm thick, preferably 80-90 nm, with the silver layer being less than 20 nm thick, preferably 15 nm, and the two oxide layers being less than 50 nm thick, preferably between 30-40 nm.
4. The layer system according to claim 4, characterized in that the oxide layers contain indium and cerium, preferably 90-95 at. % indium and 5-10 at. % cerium.
5. The layer system according to claim 5, characterized in that the silver layer contains up to 10 wt. % copper, preferably in the range from 0.5-3% and best of all from 0.5-1%.
6. The layer system according to claim 1, characterized in that the layer system is less than 100 nm thick, preferably 80-90 nm, with the silver layer being less than 20 nm thick, preferably 15 nm, and the two oxide layers being less than 50 nm thick, preferably between 30-40 nm.
7. The layer system according to claim 6, characterized in that the oxide layers contain indium and cerium, preferably 90-95 at. % indium and 5-10 at. % cerium.

8. The layer system according to claim 7, characterized in that the silver layer contains up to 10 wt. % copper, preferably in the range from 0.5-3% and best of all from 0.5-1%.

9. The layer system according to claim 1, characterized in that the oxide layers contain indium and cerium, preferably 90-95 at. % indium and 5-10 at. % cerium.

10. The layer system according to claim 9, characterized in that the silver layer contains up to 10 wt. % copper, preferably in the range from 0.5-3% and best of all from 0.5-1%.

11. The layer system according to claim 1, characterized in that the silver layer contains up to 10 wt. % copper, preferably in the range from 0.5-3% and best of all from 0.5-1%.

12. A method producing a conductive transparent layer system on a substrate, by depositing a first oxide layer on the substrate, a silver layer on the first oxide layer and a second oxide layer on said silver layer, characterized in that the second oxide layer is deposited by means of pulsed DC sputtering or AC-superimposed DC sputtering.

13. The method of claim 12, characterized in that the frequency of the superimposed AC is between 1 and 50 MHz, preferably between 10 and 20 MHz.

14. The method according to claim 13, characterized in that the AC component, defined by the ratio of the DC and AC power supplies, is between 10-90%, preferably between 30-50%.

15. The method according to claim 12, characterized in that the total power density (AC and DC) is in the range from 1-3 W/cm², preferably, however, 2-2.2 W/cm².

16. The method according to claim 12, characterized in that magnetron sputtering is chosen as sputtering method.

17. A conductive, transparent layer system deposited on a substrate, comprising a first oxide layer deposited on the substrate, a silver layer deposited on said first oxide layer, and a second oxide layer deposited on said silver layer, characterized in that the oxide layers contain indium cerium oxide, preferably 90-95 at. % indium and 5-10 at. % cerium.

18. The layer system according to claim 17, characterized in that the silver layer contains up to 10 wt. % copper, preferably in the range from 0.5-3% and best of all from 0.5-1%.

19. The layer system according to claim 18, characterized in that the layer system is less than 100 nm thick, preferably 80-90 nm, with the silver layer being less than 20 nm thick, preferably 15 nm, and the two oxide layers being less than 50 nm thick, preferably between 30-40 nm.

20. The layer system according to claim 17, characterized in that the layer system is less than 100 nm thick, preferably 80-90 nm, with the silver layer being less than 20 nm thick, preferably 15 nm, and the two oxide layers being less than 50 nm thick, preferably between 30-40 nm.

21. The layer system according to claim 17 in which the second oxide layer is deposited by means of pulsed DC sputtering or AC-superimposed DC sputtering.

22. The layer system of claim 21, characterized in that the frequency of the superimposed AC is between 1 and 50 MHz, preferably between 10 and 20 MHz.

23. The layer system of claim 22, characterized in that the AC component, defined by the ratio of the DC and AC power supplies, is between 10-90%, preferably between 30-50%.

24. The layer system of claim 21, characterized in that the total power density (AC and DC) is in the range from 1-3 W/cm², preferably, however, 2-2.2 W/cm².

25. The layer system of claim 21, characterized in that magnetron sputtering is chosen as sputtering method.

CONDUCTIVE TRANSPARENT LAYERS AND METHOD FOR THEIR PRODUCTION

ABSTRACT OF THE DISCLOSURE

On a glass substrate, a base layer of indium cerium oxide is deposited, and on this a thin copper-containing silver layer, both produced by means of DC sputtering. On top there is 5 another indium cerium oxide layer, which is produced by means of AC-superimposed DC sputter deposition. This layer system boasts very low surface resistivity combined with high transparency in the visible part of the spectrum, which means it has a high Haacke quality factor.

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Fig.1

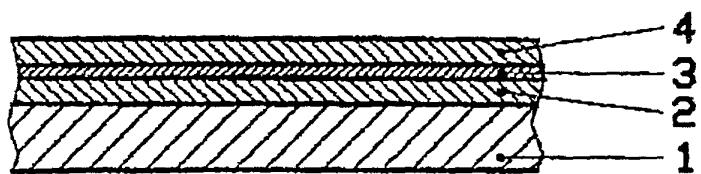


Fig.2

